Disclaimer: Although we have made every effort for this flow chart to be free of errors, it cannot be guaranteed correct for all students – check with advisor.

B.S. Computer Engineering 2019-2020 Flow Chart – Ver. 1.0

- **CPE 1000** Computer Engineering Fundamentals (2-0-2)
- **MATH 1190** Calculus I (4-0-4) placement
- **CHEM 1211& L** General Chemistry I (3-3-4)
- **Core B2** COM 1100 preferred (3-3-3)
- **ENGL 1101** Composition I (3-0-3)

**Year 1 Spring**
- **CSE 1321/L** (C++ lab) Programming and Problem Solving I (3-2-4)
- **MATH 2202** Calculus II (4-0-4)
- **PHYS 2211& L** Principles of Physics I (3-3-4)
- **EE 2301** Circuits Analysis I (3-3-4)
- **ENGL 1102** Composition II (3-0-3)

**Year 1 Fall**
- **MATH 2306** Ordinary Differential (3-0-3)
- **PHYS 2212 & L** Principles of Physics II (3-3-4)
- **EE 2501** Digital Logic Design (3-3-4)
- **EE 3401** Engineering Electronics (3-3-4)
- **Core C2** Art and Culture (3-0-3)
- **Core E4** Social Science (3-0-3)

**Year 2 Spring**
- **MATH 2332** Probability and Data (3-0-3)
- **ENGR 3325** Engineering Economic Analysis (3-0-3)
- **CPE 3000** Comp Organization Interfacing (3-3-4)
- **CPE 3030** Advanced Embedded Design (3-3-4)
- **Core E1** POLS 1101 (3-0-3)

**Year 2 Fall**
- **MATH 2335** Numerical Method (3-0-3)
- **CPE 3040** Interface Communications (3-3-4)
- **EE 4201** Control Systems (3-3-4)
- **CPE 4040** Data Collection & Analyses (2-3-3)
- **Core E2** US History (3-0-3)

**Year 3 Spring**
- **MATH 2345** Discrete Mathematics (3-0-3)
- **CPE 4010** Sensors, Actuators, Integration (3-3-4)
- **CPE 3020** VHDL Design With FPGAs (3-3-4)
- **CPE 4800** Senior Project Proposal (2-0-2)
- **Core E3** World History (3-0-3)

**Year 3 Fall**
- **Engineering Elective** (CPE/EE/ME/MCRE, SWE 36x3, CS 3305)
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- **Engineering Elective** (CPE/EE/ME/MCRE, SWE 36x3, CS 3305)

**Year 4 Spring**
- **EE 4201** Control Systems (3-3-4)
- **CPE 3020** VHDL Design With FPGAs (3-3-4)
- **CPE 4800** Senior Project Proposal (2-0-2)
- **CPE 4850** Senior Project Design (1-6-3)
- **CPE 4020** Device Networks (3-3-4)

**Year 4 Fall**
- **Engineering Elective** (CPE/EE/ME/MCRE, SWE 36x3, CS 3305)
- **CPE 3040** Interface Communications (3-3-4)
- **CPE 4800** Senior Project Proposal (2-0-2)
- **CPE 4850** Senior Project Design (1-6-3)
- **CPE 4020** Device Networks (3-3-4)

* MTRE 4001, EE3501, EE 3706 not allow as electives; Pre-reqs for SWE and CS are CS1311 or 1321 and CPE 3000