B.S. Computer Engineering 2018 Flow Chart – Ver. 2.4 – Pending Approval for Fall 2018

Disclaimer: Although we have made every effort for this flow chart to be free of errors, it cannot be guaranteed correct for all students – check with advisor. This version is pending approval for Fall 2018 catalog.

Year 1

Fall
- ENGL 1101 Composition I (3-0-3)
- MATH 1190 Calculus I (4-0-4)
- CHEM 1211& L General Chemistry I (3-3-4)
- Core B2
- ENGL 1101 Composition I (3-0-3)

Spring
- CSE 1311 C++ Programming for Engineers (3-2-4)
- MATH 2202 Calculus II (4-0-4)
- PHYS 2211& L Principles of Physics I (3-3-4)
- EE 2501 Digital Logic Design (3-3-4)
- ENGL 1102 Composition II (3-0-3)

Year 2

Fall
- MATH 2306 Ordinary Differential (3-0-3)
- PHYS 2212 & L Principles of Physics II (3-3-4)
- EE 3401 Engineering Electronics (3-3-4)
- Core C1
- Core C2

Spring
- MATH 2332 Probability and Data (3-0-3)
- MATH 2305 Numerical Method (3-0-3)
- ENGR 3325* Engineering Economic Analysis (3-3-3)
- CPE 3000* Comp Organization Interfacing (3-3-4)
- Core C3

Year 3

Fall
- Engineering Elective* (CPE/EE/ME/MTRE, SWE 36x3, CS 3304)
- MATH 2345 Discrete Mathematics (3-0-3)
- CPE 4010* Sensors, Actuators, Integration (3-3-4)
- CPE 4040* Data Organization & Analytics (2-3-3)
- Core E1
- Core E2

Spring
- Engineering Elective** (CPE/EE/ME/MTRE, SWE 36x3, CS 3304)
- EE 4201* Control Systems (3-3-4)
- CPE 3020* VHDL Design With FPGAs (3-3-4)
- CPE 4800* Senior Project Proposal (2-0-2)
- Core E3

Year 4

Fall
- Engineering Elective* (CPE/EE/ME/MTRE, SWE 36x3, CS 3304)
- CPE 3040* Interfacing & Communications (3-3-4)
- CPE 3040* Interfacing & Communications (3-3-4)
- CPE 4850* Senior Project Design (1-6-3)
- CPE 4020* Device Networks (3-3-4)

Spring
- Engineering Elective** (CPE/EE/ME/MTRE, SWE 36x3, CS 3304)
- EE 4201* Control Systems (3-3-4)
- CPE 3020* VHDL Design With FPGAs (3-3-4)
- CPE 4800* Senior Project Proposal (2-0-2)
- Core E3

Notes:
- Red = pending approval for Fall 2018 - Override
- Requires Engineering Standing
- Co-requisite
- Pre-requisite
- Critical Path

Requirements:
- Core B1
- Core B2
- Core B3
- Core B4
- Core C1
- Core C2
- Core C3
- Core C4
- Core E1
- Core E2
- Core E3
- Core E4
- Core E5

Special Notes:
- Placement required for MATH 1190
- MATH 2306 is a co-requisite of PHYS 2212
- MATH 2345 is a co-requisite of ENGR 3325
- ENGR 3325 is a co-requisite of CPE 3000
- CPE 3000 is a co-requisite of MATH 2345

Course Descriptions:
- ENGL 1101 Composition I
- MATH 1190 Calculus I
- CHEM 1211& L General Chemistry I
- Core B2
- ENGL 1101 Composition I
- CSE 1311 C++ Programming for Engineers
- MATH 2202 Calculus II
- PHYS 2211& L Principles of Physics I
- EE 2501 Digital Logic Design
- ENGL 1102 Composition II
- MATH 2306 Ordinary Differential
- PHYS 2212 & L Principles of Physics II
- EE 3401 Engineering Electronics
- Core C1
- Core C2
- MATH 2332 Probability and Data
- MATH 2305 Numerical Method
- ENGR 3325 Engineering Economic Analysis
- CPE 3000 Comp Organization Interfacing
- CPE 4010 Sensors, Actuators, Integration
- Core C3
- Core E1
- Core E2
- Engineering Elective* (CPE/EE/ME/MTRE, SWE 36x3, CS 3304)
- Engineering Elective** (CPE/EE/ME/MTRE, SWE 36x3, CS 3304)
- EE 4201 Control Systems
- CPE 3020 VHDL Design With FPGAs
- CPE 4850 Senior Project Design
- CPE 4020 Device Networks
- CPE 3040 Interfacing & Communications
- CPE 4040 Data Organization & Analytics
- EE 2211 Principles of Physics I
- EE 2212 Principles of Physics II
- EE 2301 Circuits Analysis I
- EE 2501 Digital Logic Design
- EE 3401 Engineering Electronics
- Core E2
- Core E3
- Core E4
- ECON 1000 Contemporary Economic Issues
- MATH 2345 Discrete Mathematics
- MATH 3355 Numerical Method
- ENGR 3325 Engineering Economic Analysis
- CPE 3000 Comp Organization Interfacing
- CPE 4010 Sensors, Actuators, Integration
- CPE 3030 Advanced Embedded Design
- CPE 4040 Data Organization & Analytics
- EE 4201 Control Systems
- CPE 3020 VHDL Design With FPGAs
- CPE 4850 Senior Project Proposal
- CPE 4020 Device Networks
- CPE 3040 Interfacing & Communications